	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	20385	multiprocess$3	US-PGPUB; USPAT	2005/03/15 18:45		
2	BRS	L2	28740	(multiple plurality) adj3 processor$1	US-PGPUB; USPAT	2005/03/15 18:46		
3	BRS	L3	80930	(integrated adj circuit) same ("4" or "5")	US-PGPUB; USPAT	2005/03/15 18:47		
4	BRS	L4	1626	(integrated adj circuit) same (1 or 2)	US-PGPUB; USPAT	2005/03/15 18:57		
5	BRS	L5	10515	wireless adj (transceiver receiver transmitter)	US-PGPUB; USPAT	2005/03/15 18:58		
6	BRS	L6	19	4 and 5	US-PGPUB; USPAT	2005/03/15 19:30		
7	BRS	L7	2955	chip same (1 or 2)	US-PGPUB; USPAT	2005/03/15 19:31		
8	BRS	L8	34	5 and 7	US-PGPUB; USPAT	2005/03/15 19:31		
9	BRS	L9	26	8 not 6	US-PGPUB; USPAT	2005/03/15 19:31		